itle: Semice Actor Device Having Protection Circuit Implement Bipola Transistor for Discharging Static Charge Current and Process of Fabrication Inventor: Kaoru NARITA

U.S. Appln. No. 09/421,273

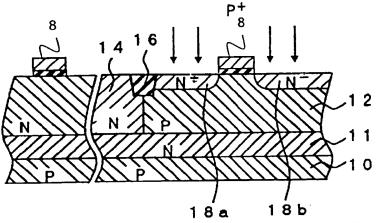


Fig. 17E

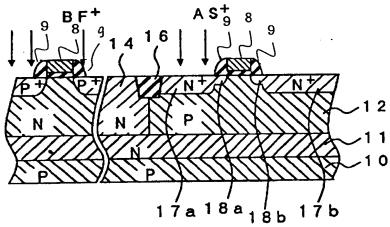


Fig. 17F

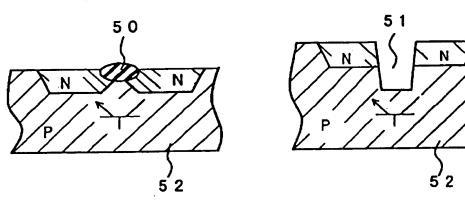
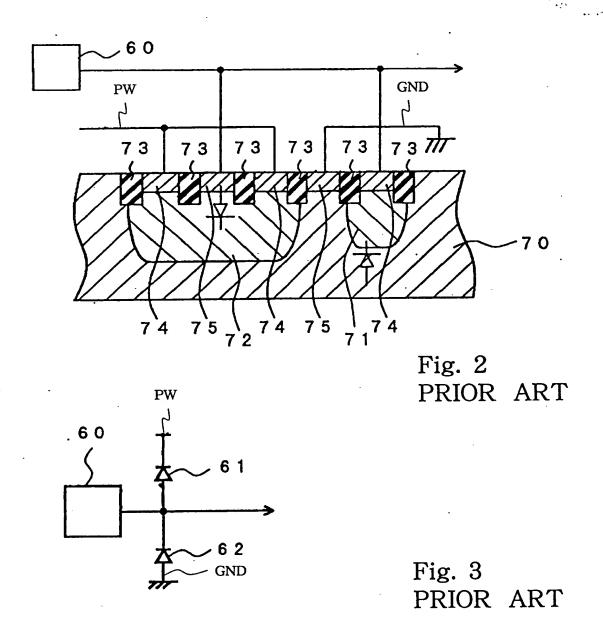


Fig. 1A PRIOR ART

Fig. 1B PRIOR ART

Title: Semiconauctor Device Having Protection Circuit Implemented Bipolar Transistor for Discharging Static Charge Current and Process of Fabrication Inventor: Kaoru NARITA
U.S. Appln. No. 09/421,273



Title: Semiconauctor Device Having Protection Circuit Implementes. y Bipolar Transistor for Discharging Static Charge Current and Process of Fabrication Inventor: Kaoru NARITA U.S. Appln. No. 09/421,273

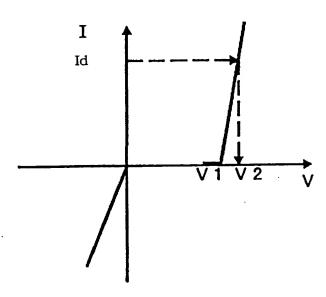


Fig. 4 PRIOR ART

Title: Semiconductor Device Having Protection Circuit Implemented Bipolar Transistor for Discharging Static Charge Current and Process of Fabrication Inventor: Kaoru NARITA
U.S. Appln. No. 09/421,273

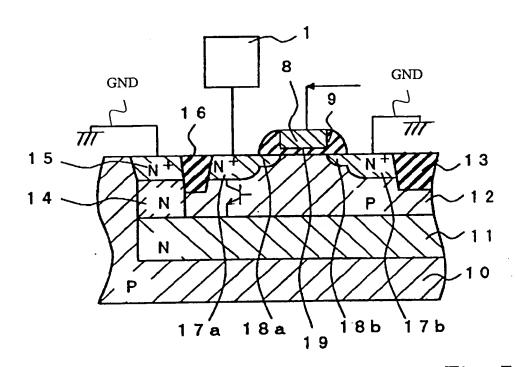




Fig. 6

Title: Semiconductor Device Having Protection Circuit Implemente Bipola Transistor for Discharging Static Charge Current and Process of Fabrication Inventor: Kaoru NARITA
U.S. Appln. No. 09/421,273 y Bipolar

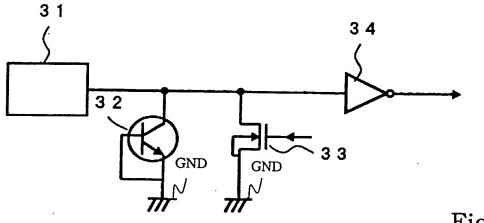
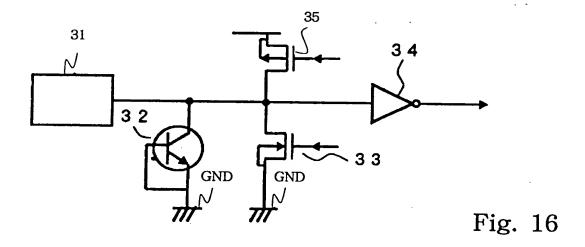
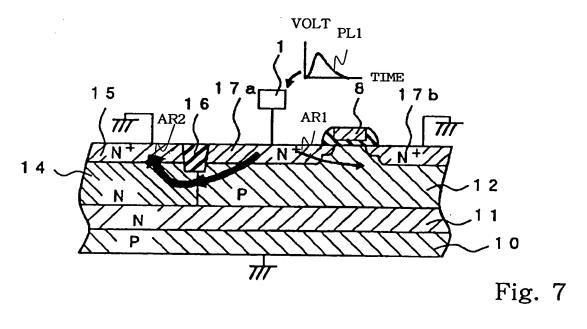


Fig. 15





Title: Semiconductor Device Having Protection Circuit Implemented Bipolar Transistor for Discharging Static Charge Current and Process of Fabrication Inventor: Kaoru NARITA U.S. Appln. No. 09/421,273

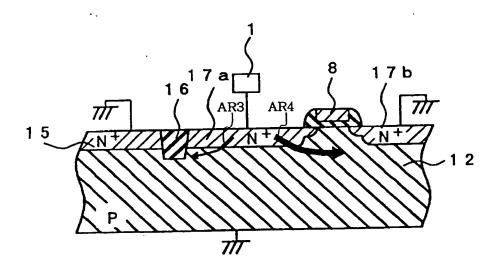
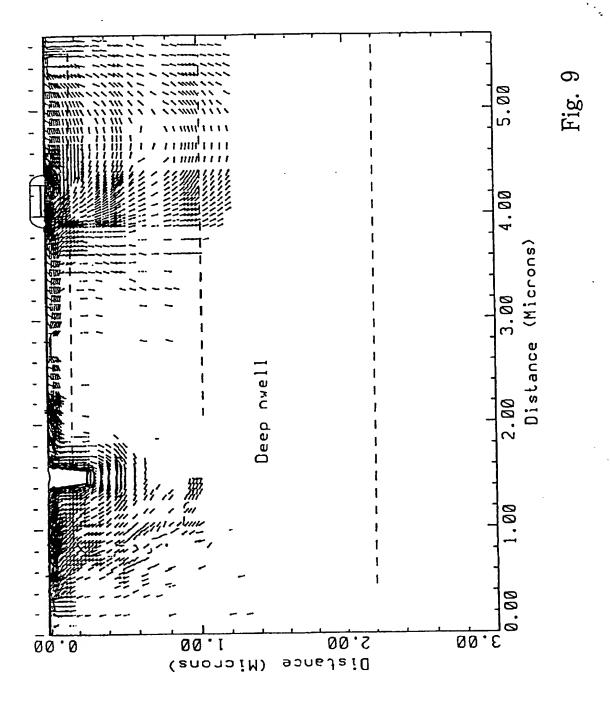
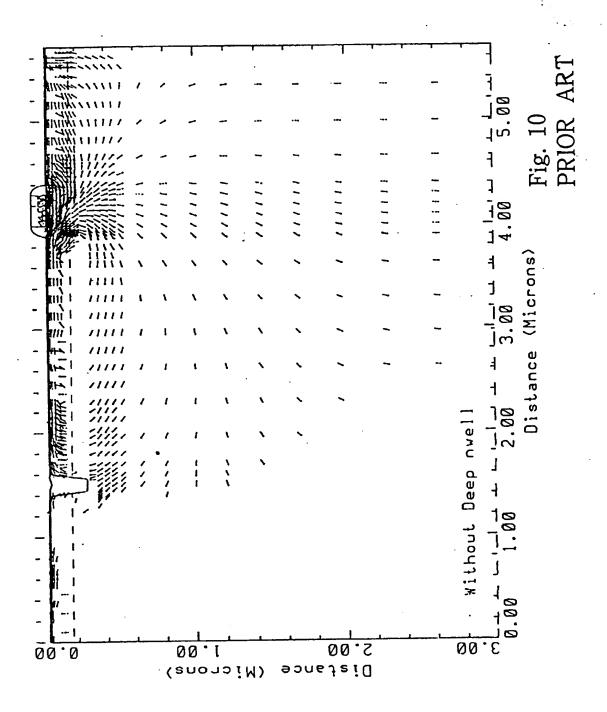
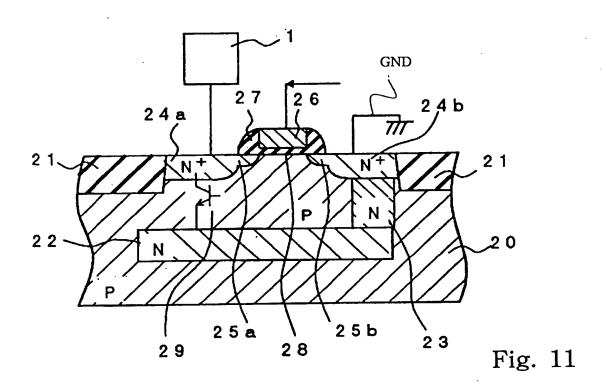


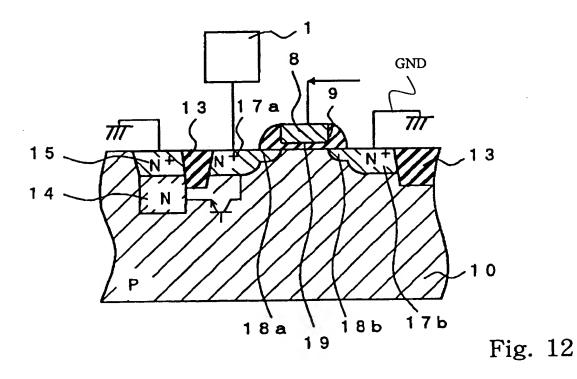
Fig. 8 PRIOR ART

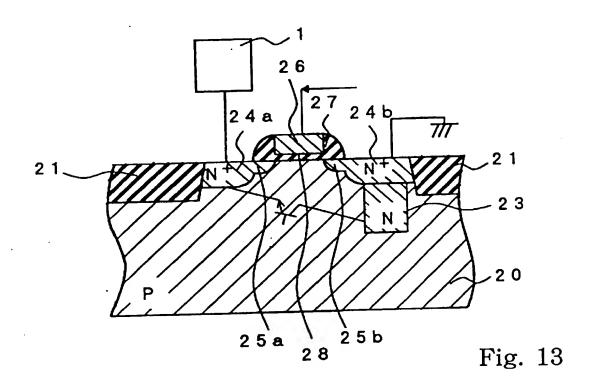


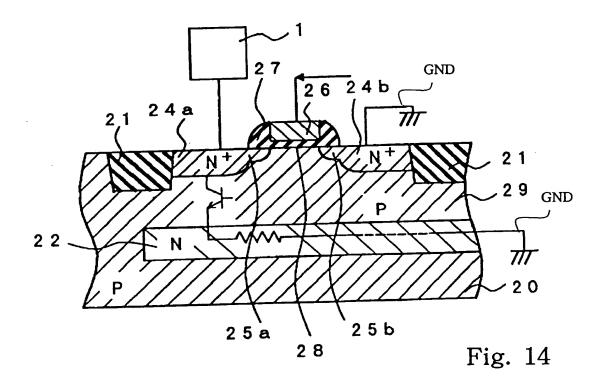


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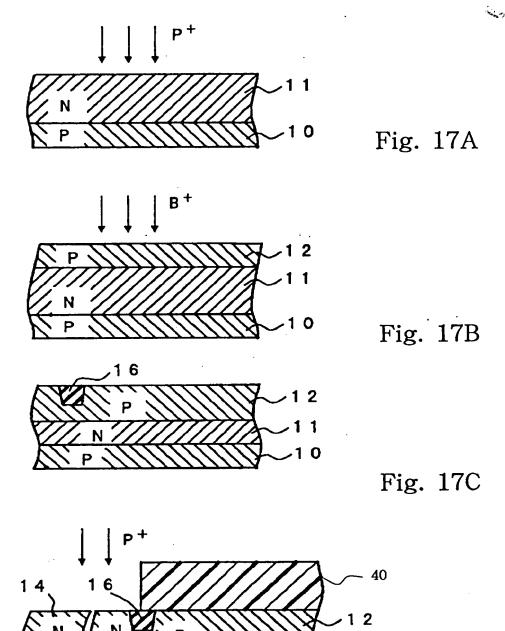


Fig. 17D